



10/02/80

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED  Q.A. </p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/932891	CONT/PRIOR F	CLASS 370 713	SUBCLASS 500	ART UNIT 2661 282	EXAMINER LEE
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APPLICANTS

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3741

Variable clock configuration for switched op-amp circuits

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____ _____	
	_____ (Primary Examiner)		ISSUE FEE	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
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